

FIG. 3

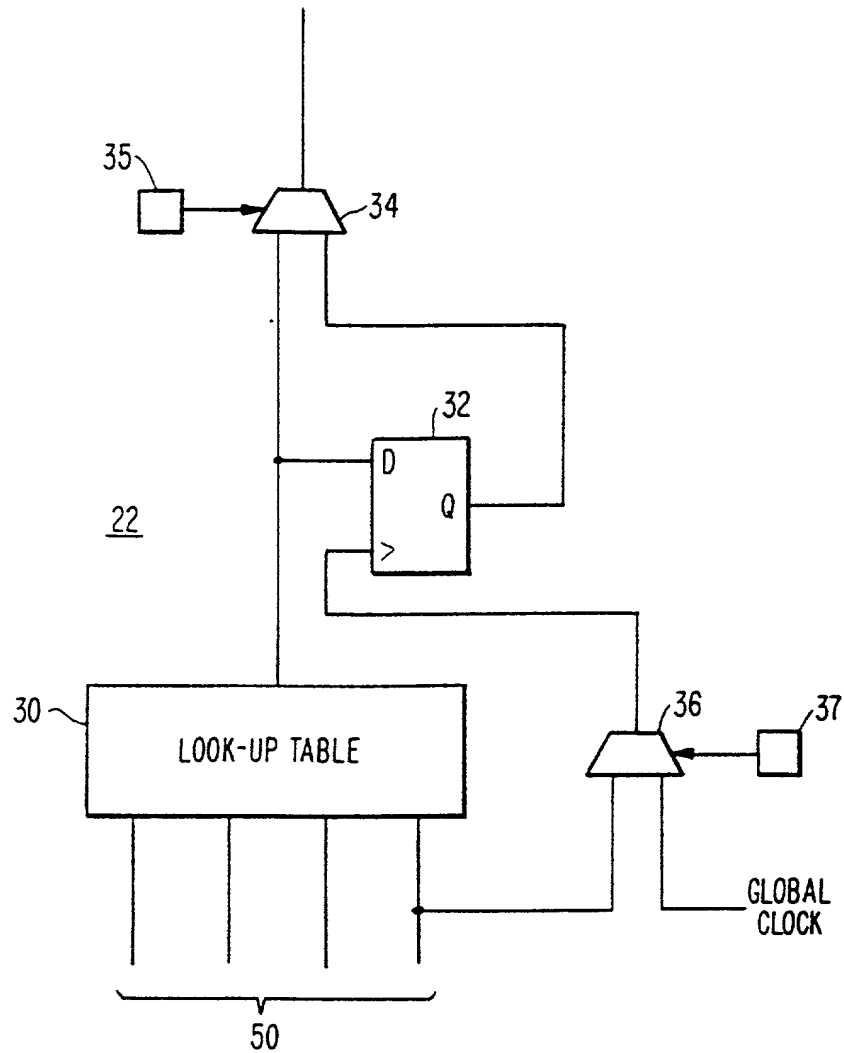


FIG. 4

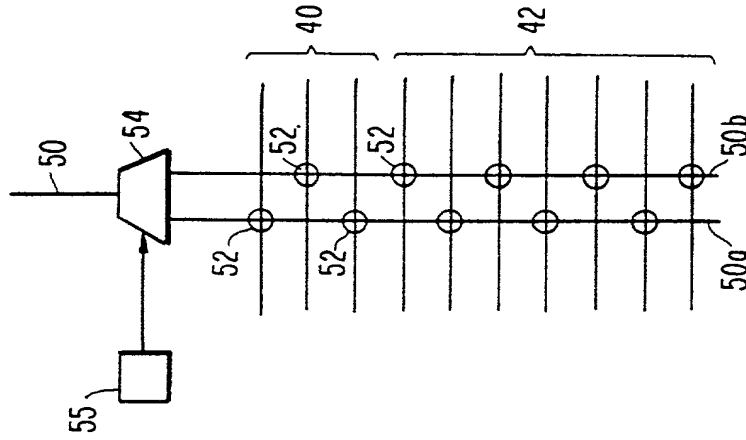


FIG. 6

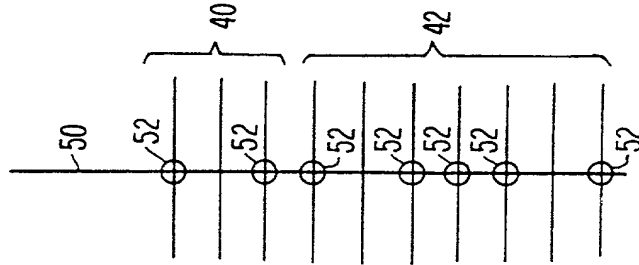


FIG. 5

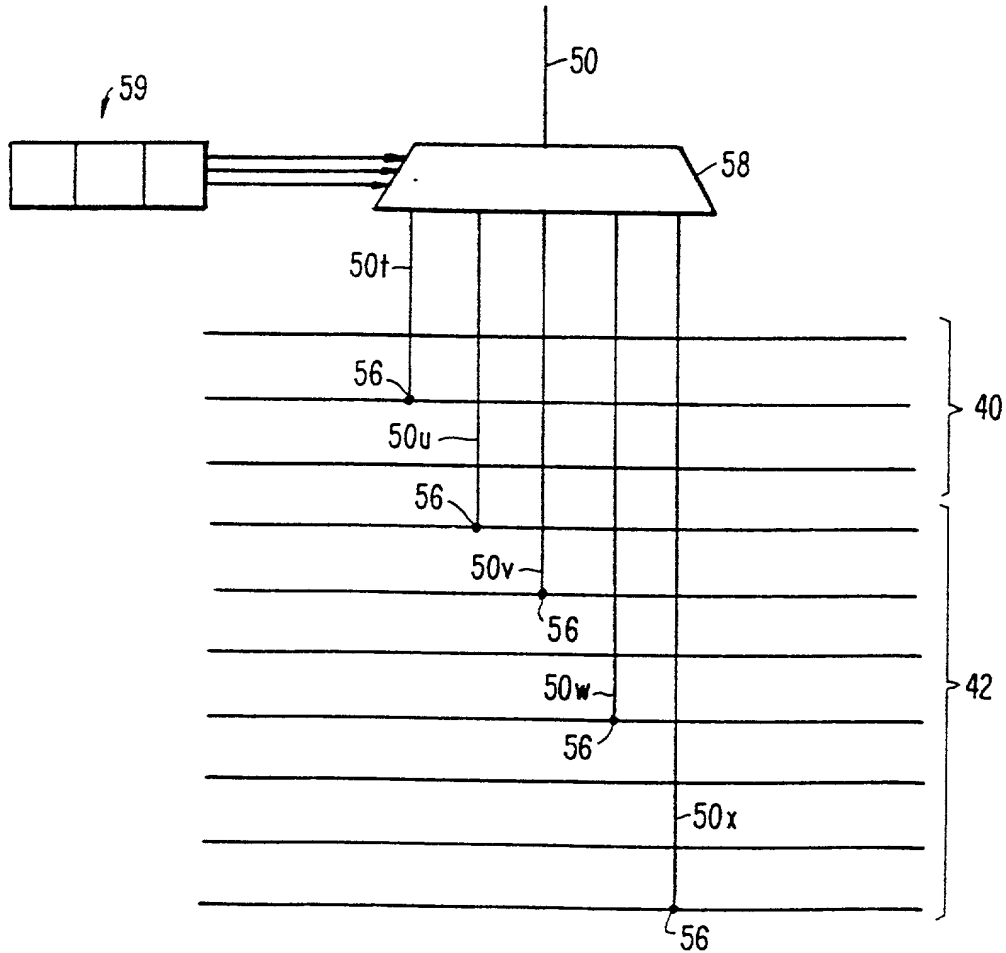


FIG. 7

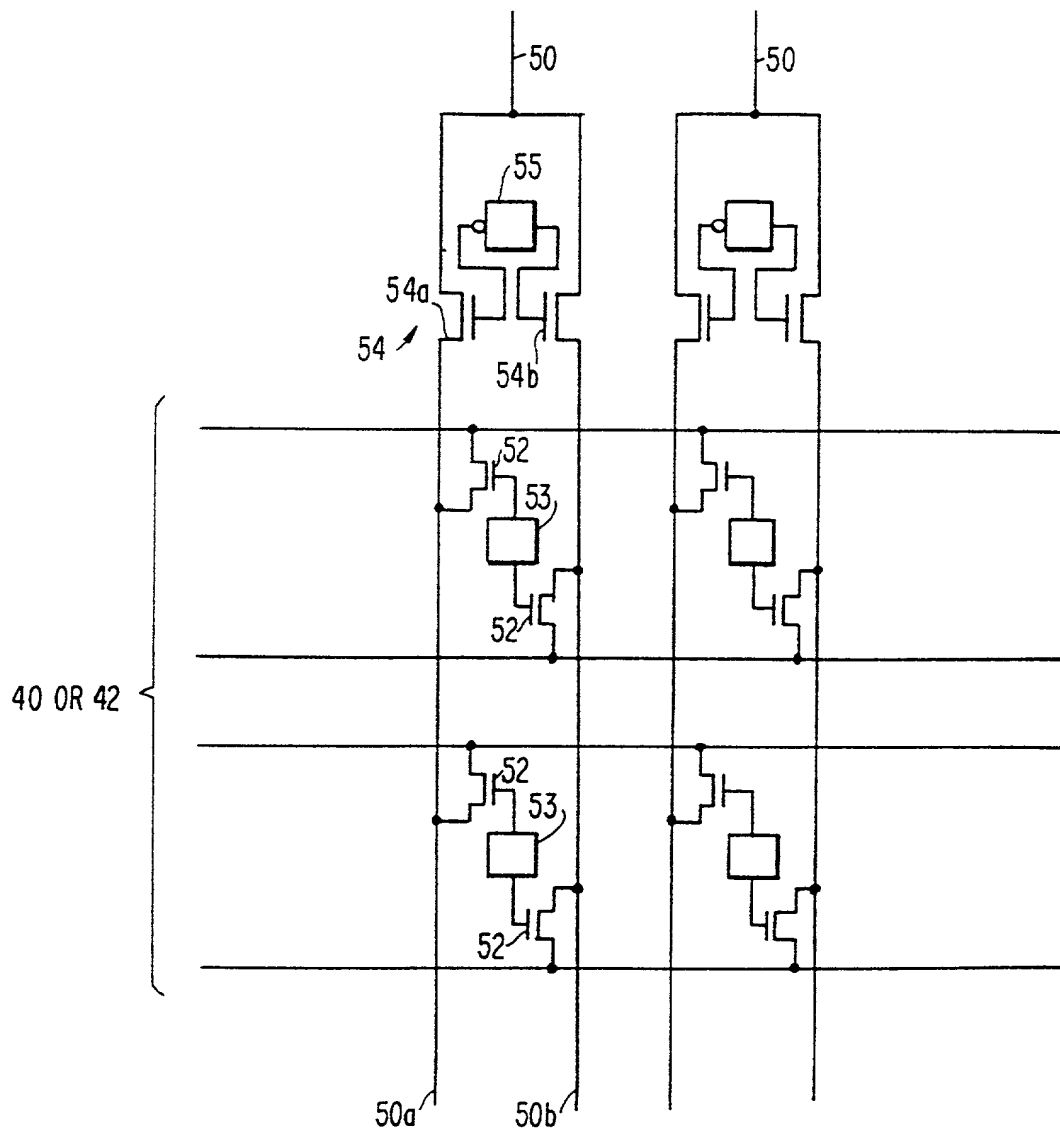


FIG. 8

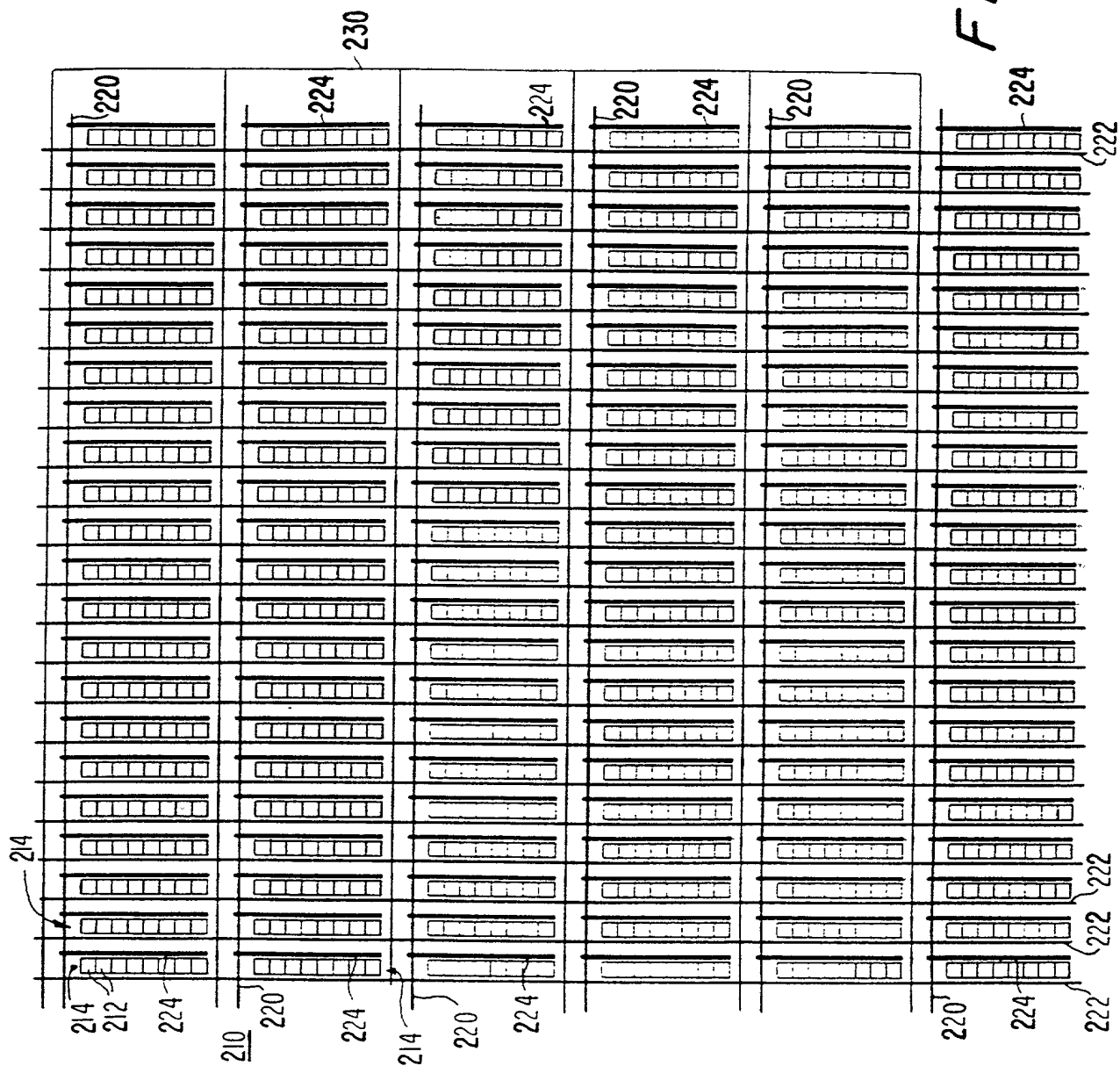




**Title:** *Melvin D. Chan, Reg. No. 39,626 (650) 326-2400*  
**PROGRAMMABLE LOGIC  
ARRAY INTEGRATED CIRCUITS**

**Inventor:** CLIFF, Richard G.  
**Attorney Docket:** 15114-032993US  
**Application Number:** Unassigned  
**Filing Date:** August 22, 2001  
**Formal Drawing:** Sheet 9 of 27

|                            |                   |
|----------------------------|-------------------|
| <b>Inventor:</b>           | CLIFF, Richard G. |
| <b>Attorney Docket:</b>    | 15114-032993US    |
| <b>Application Number:</b> | Unassigned        |
| <b>Filing Date:</b>        | August 22, 2001   |
| <b>Formal Drawing:</b>     | Sheet 9 of 27     |



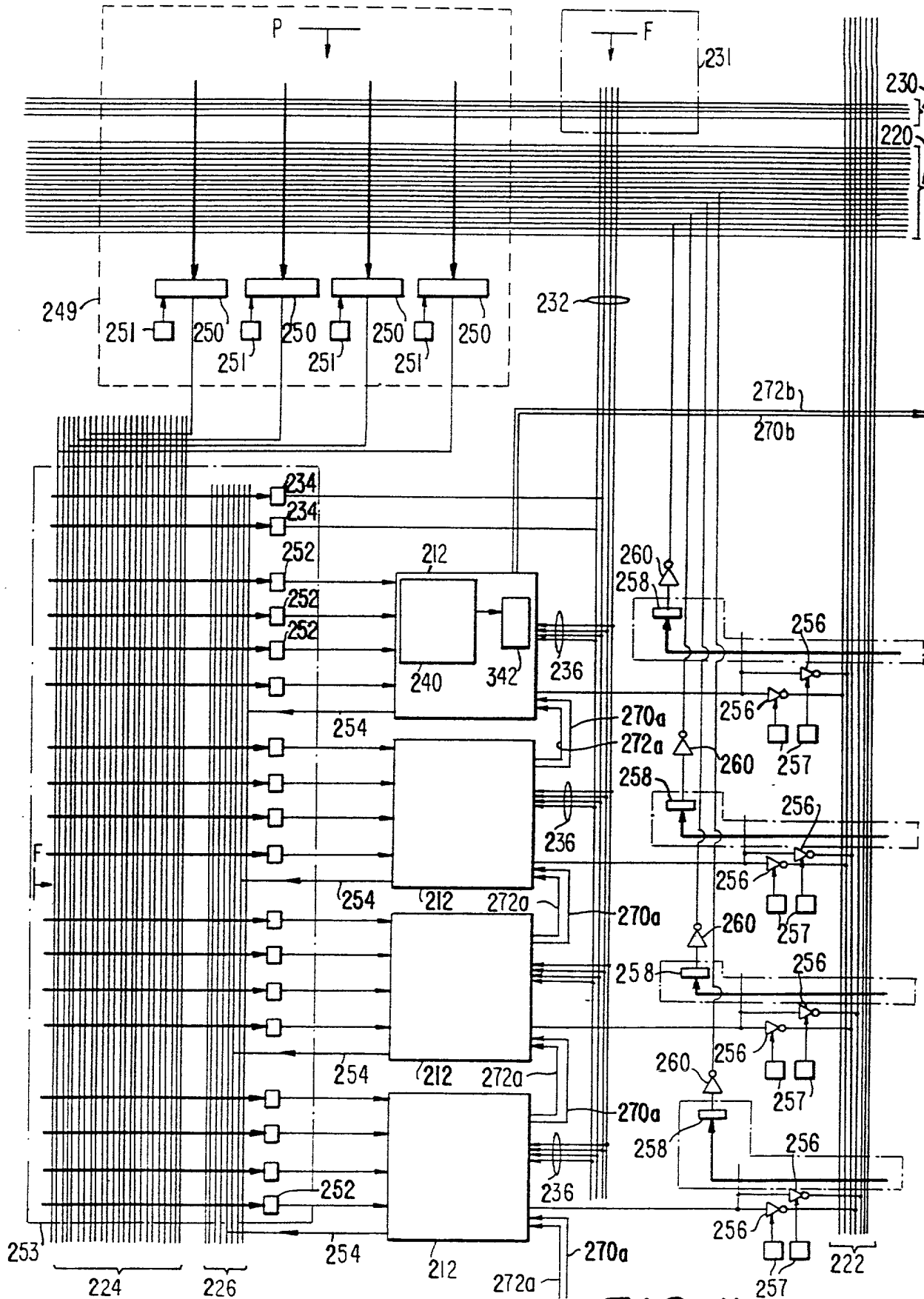


FIG. 11.



240

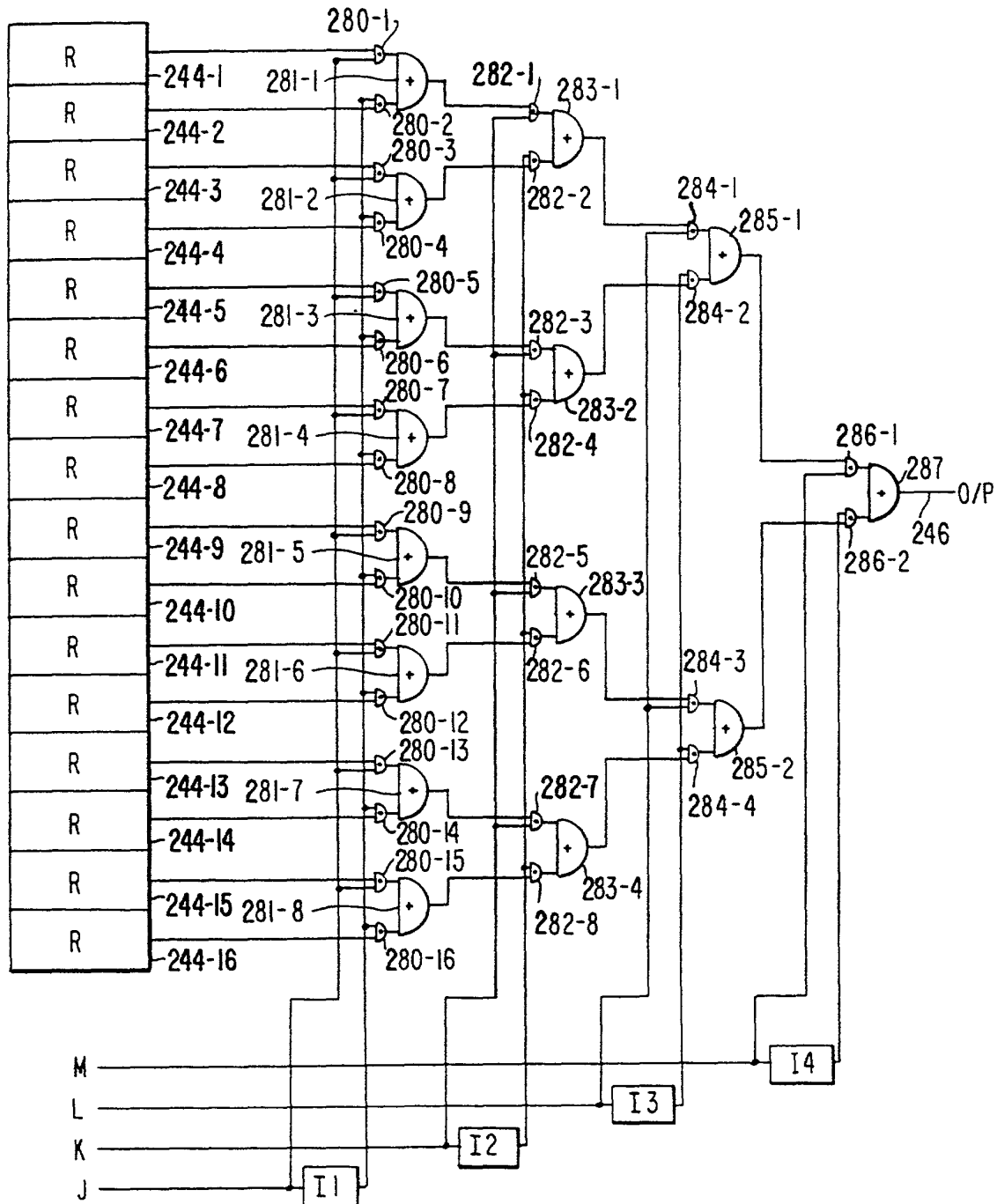


FIG. 13.  
 PRIOR ART

|                            |                   |
|----------------------------|-------------------|
| <b>Inventor:</b>           | CLIFF, Richard G. |
| <b>Attorney Docket:</b>    | 15114-032993US    |
| <b>Application Number:</b> | Unassigned        |
| <b>Filing Date:</b>        | August 22, 2001   |
| <b>Formal Drawing:</b>     | Sheet 13 of 27    |

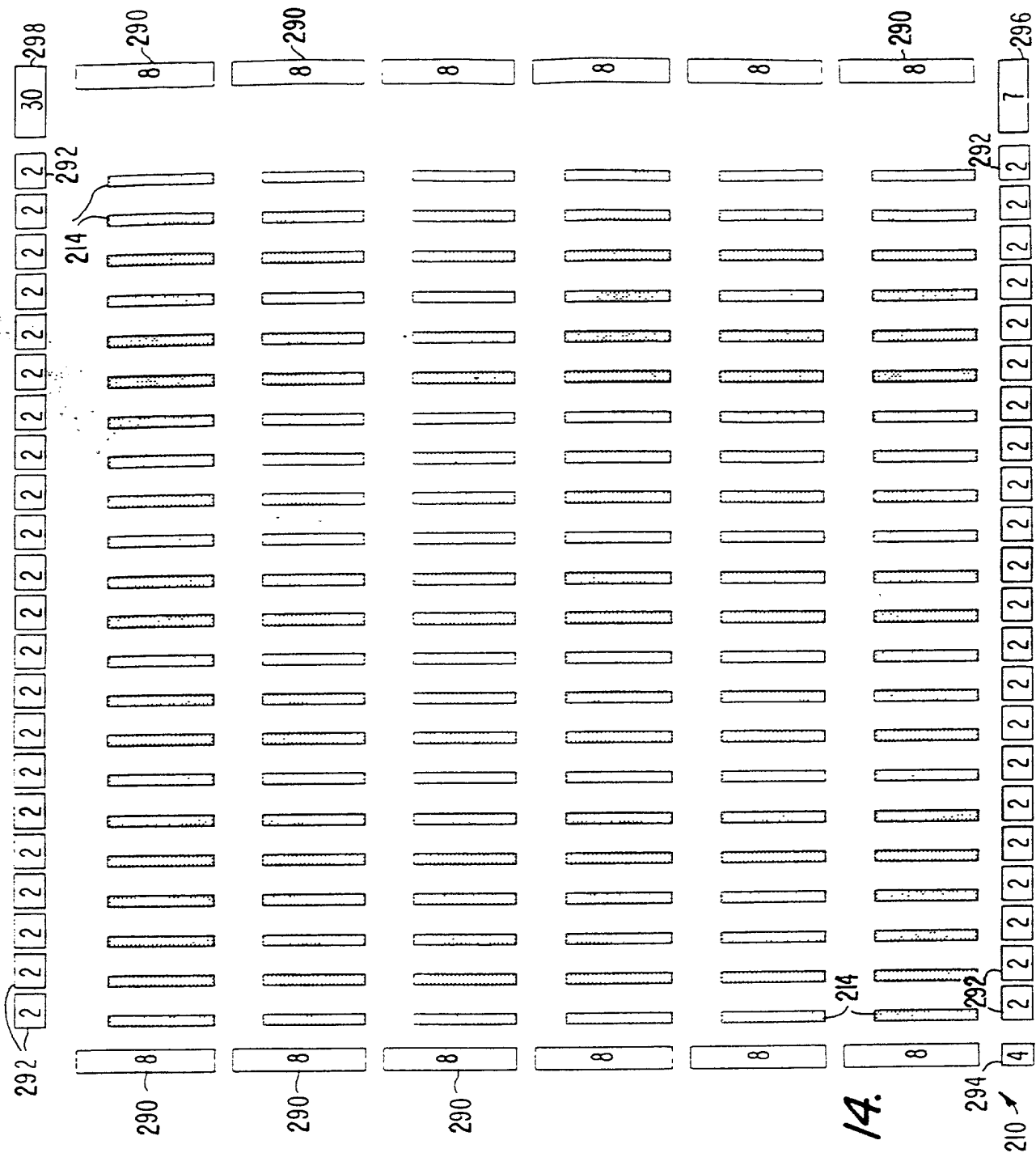
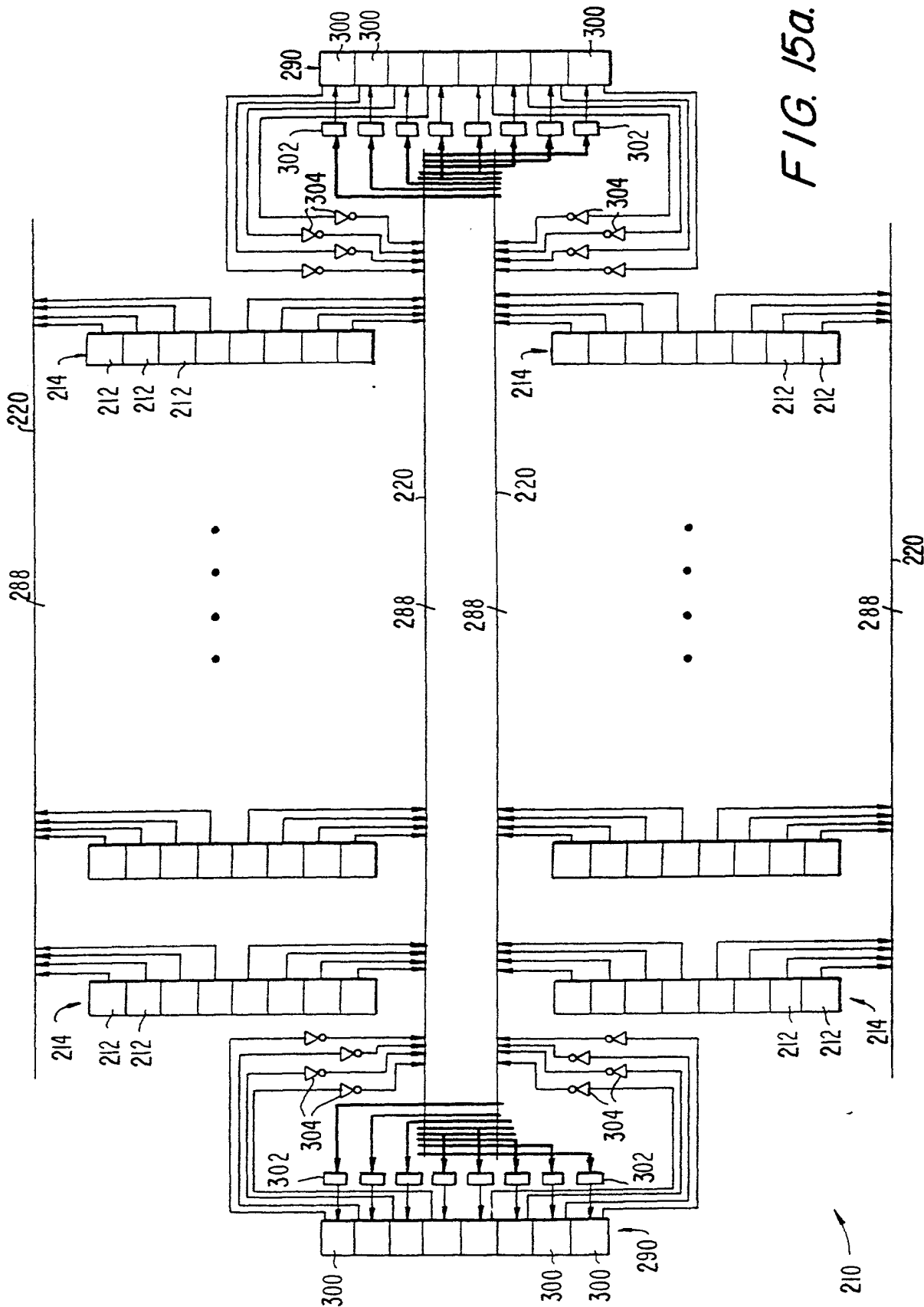


FIG. 14.



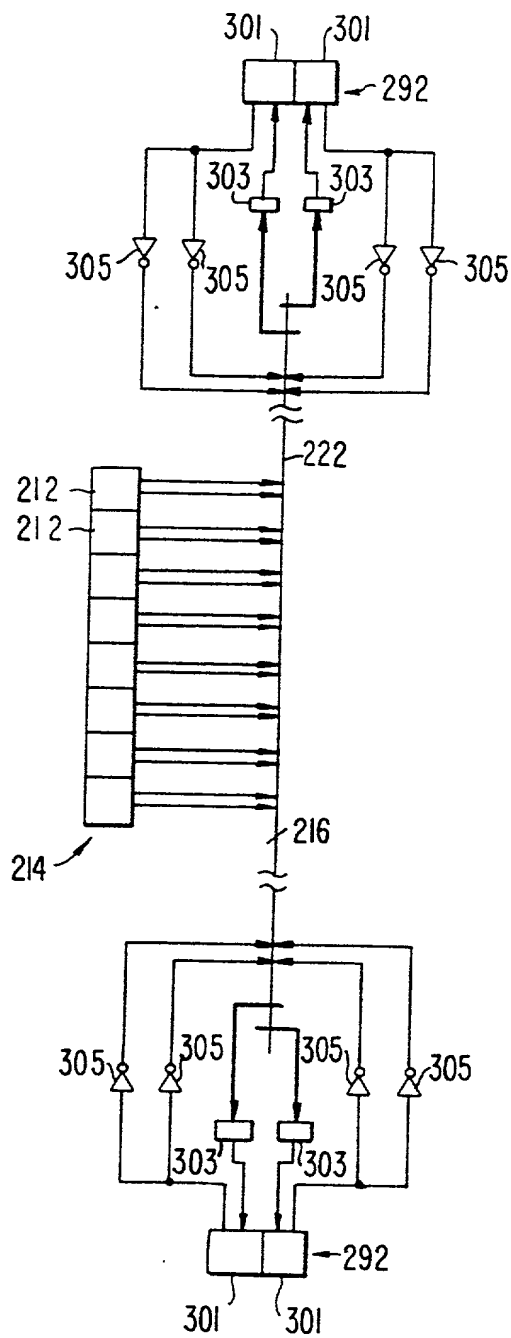


FIG. 15b.

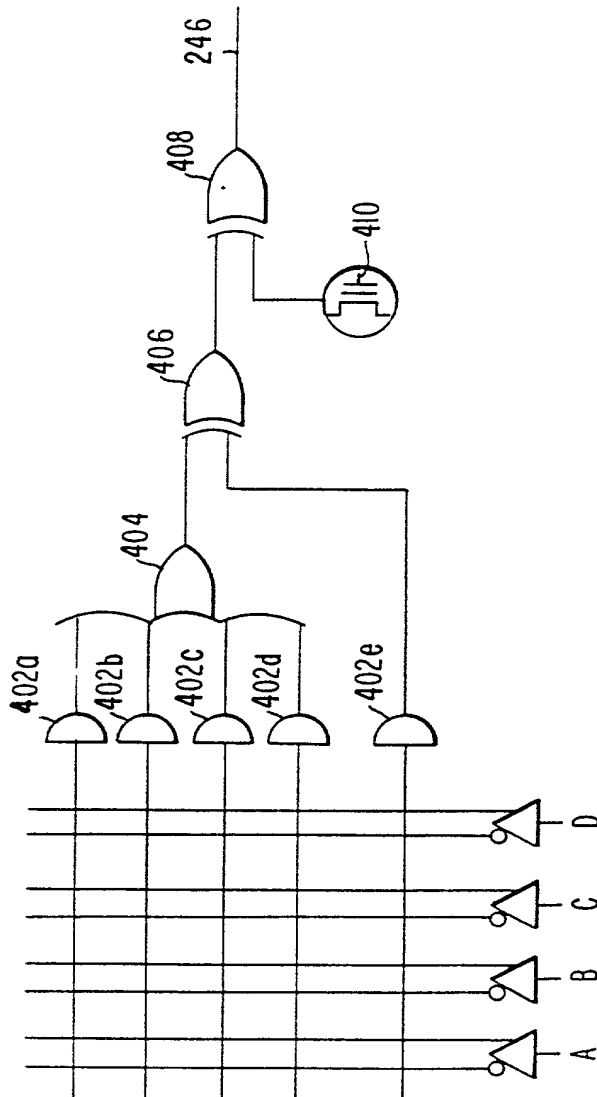


FIG. 16a.



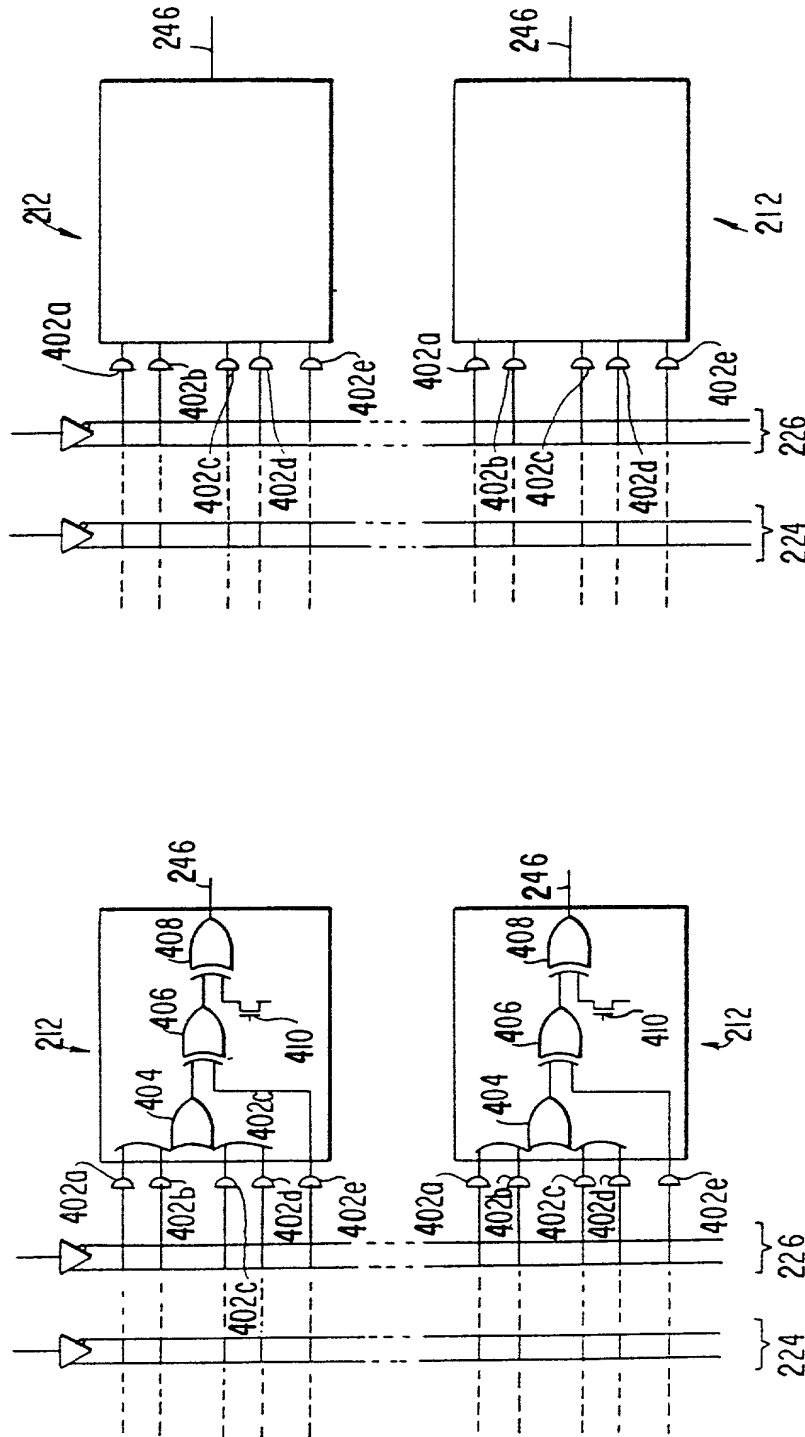


FIG. 16c.

FIG. 16b.

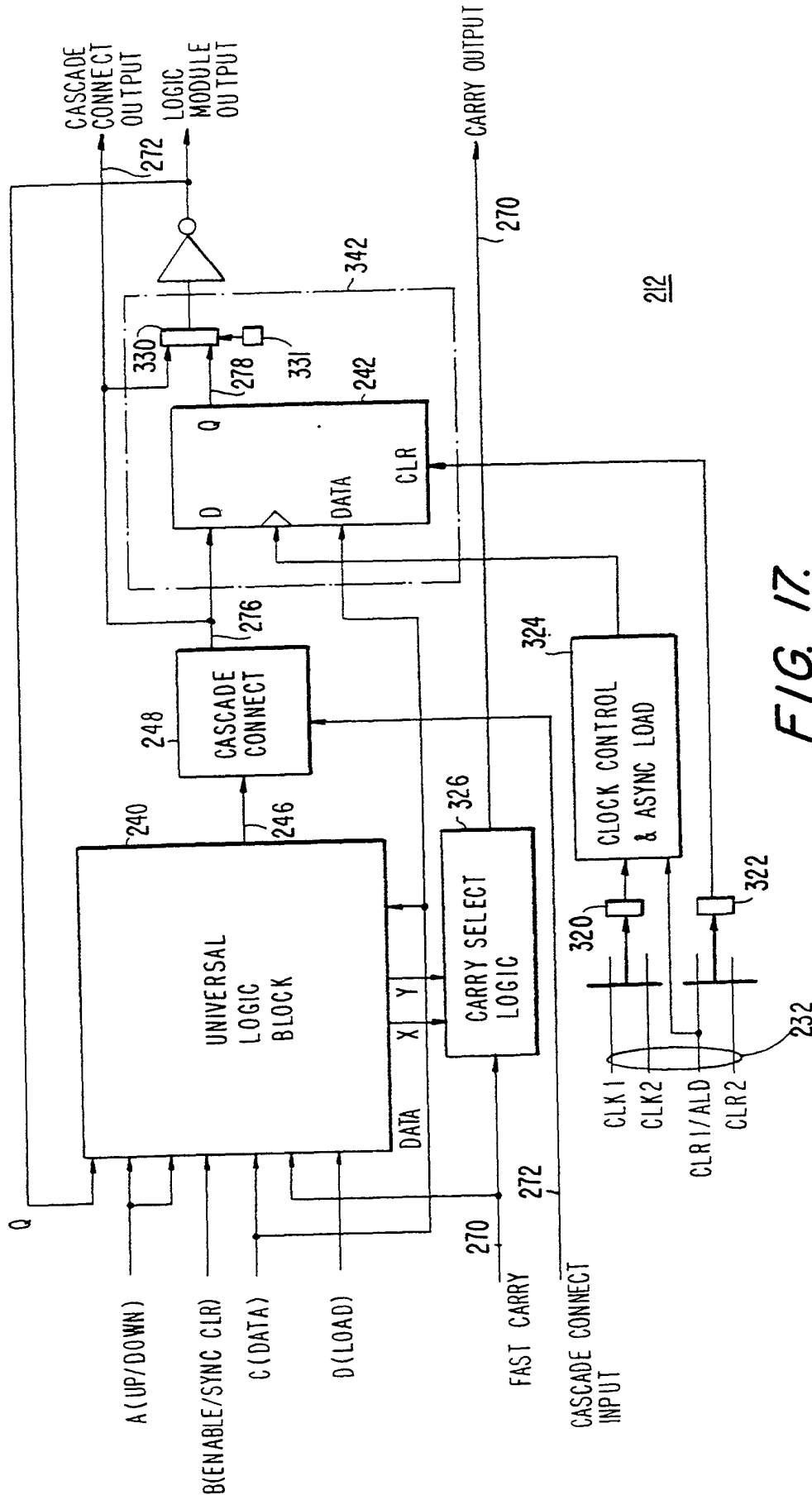


FIG. 17.



FIG. 18.

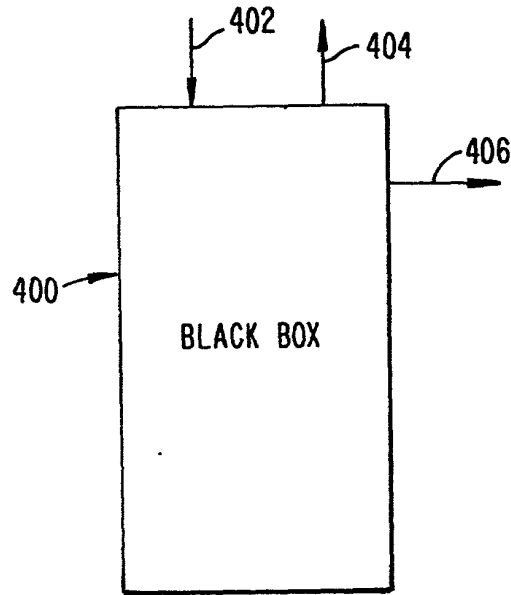


FIG. 19.

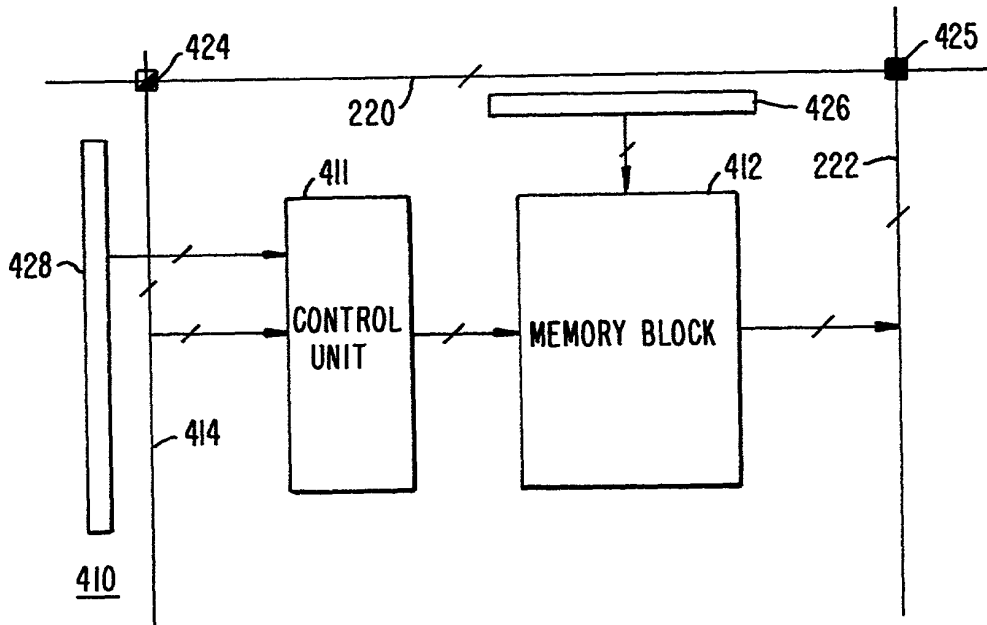


FIG. 20.

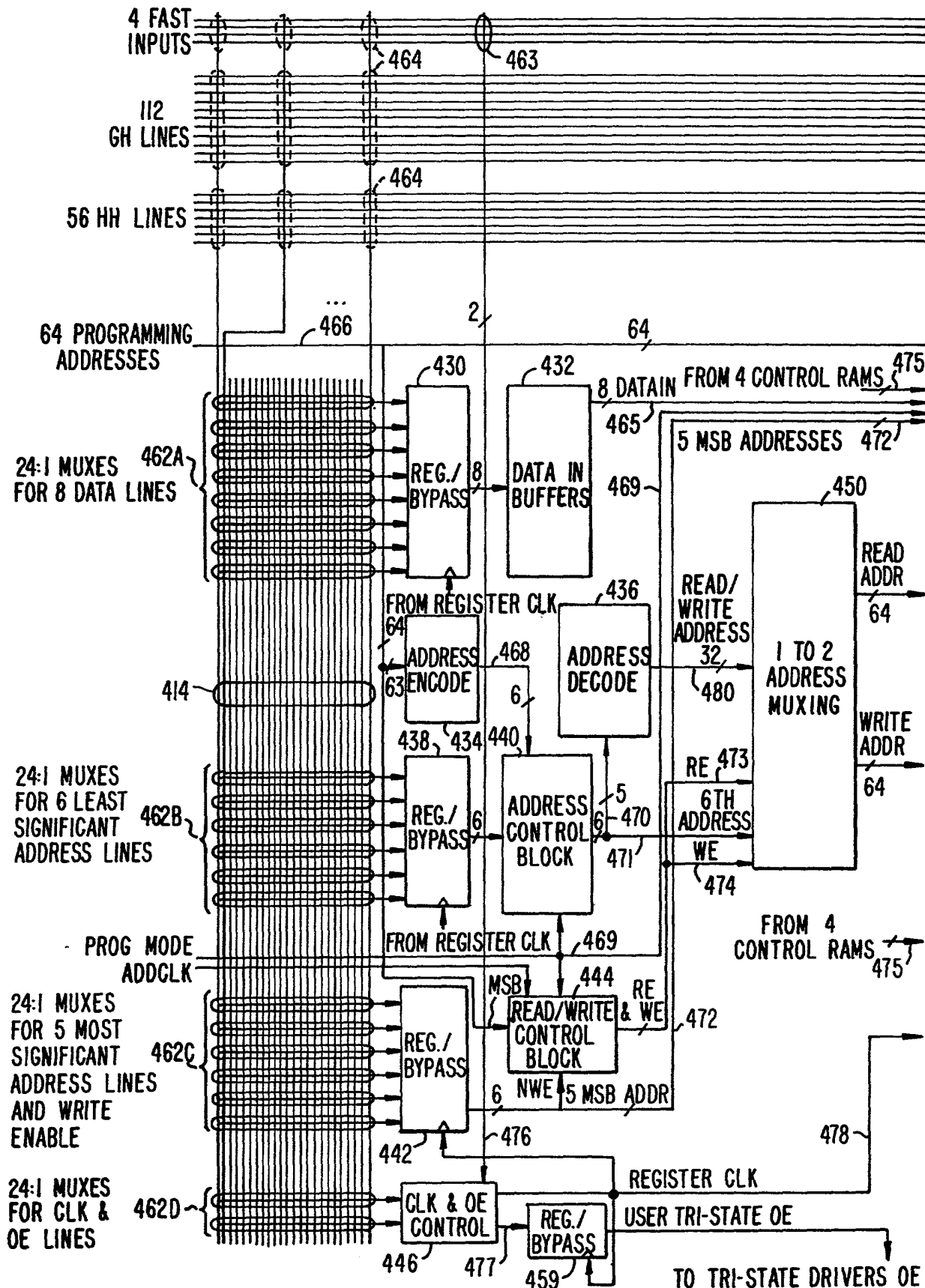


FIG. 21A.

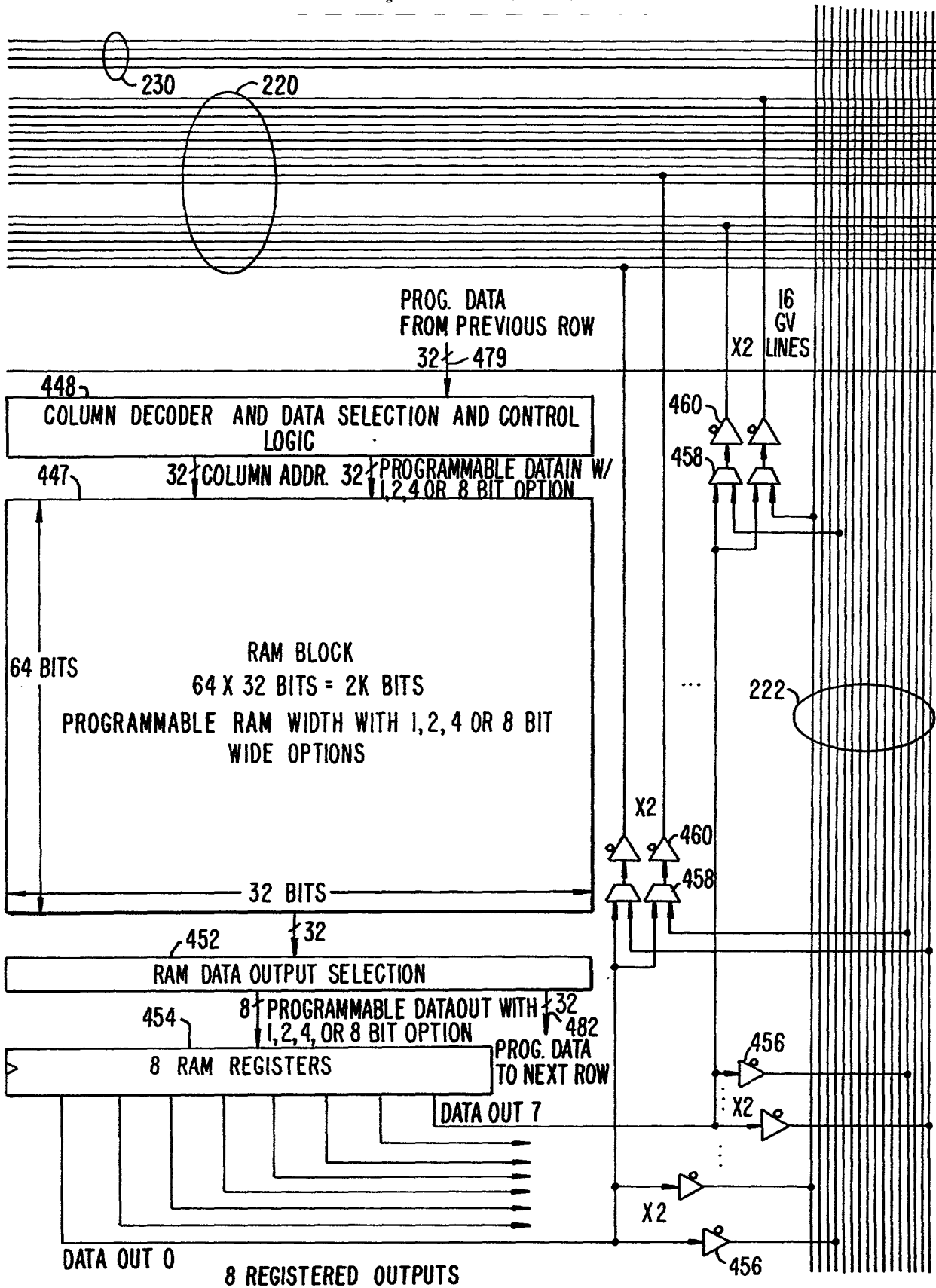
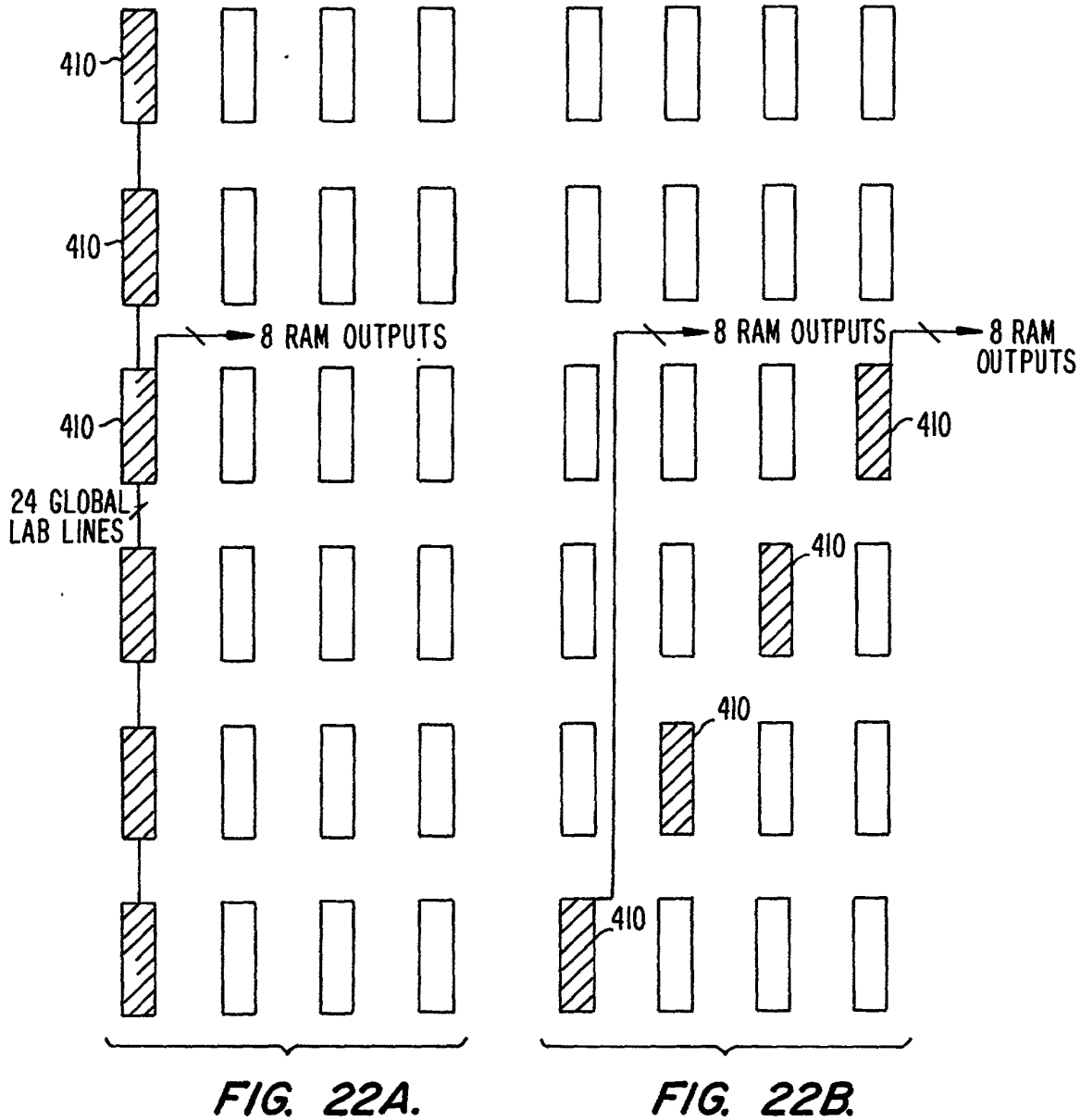


FIG. 21B.



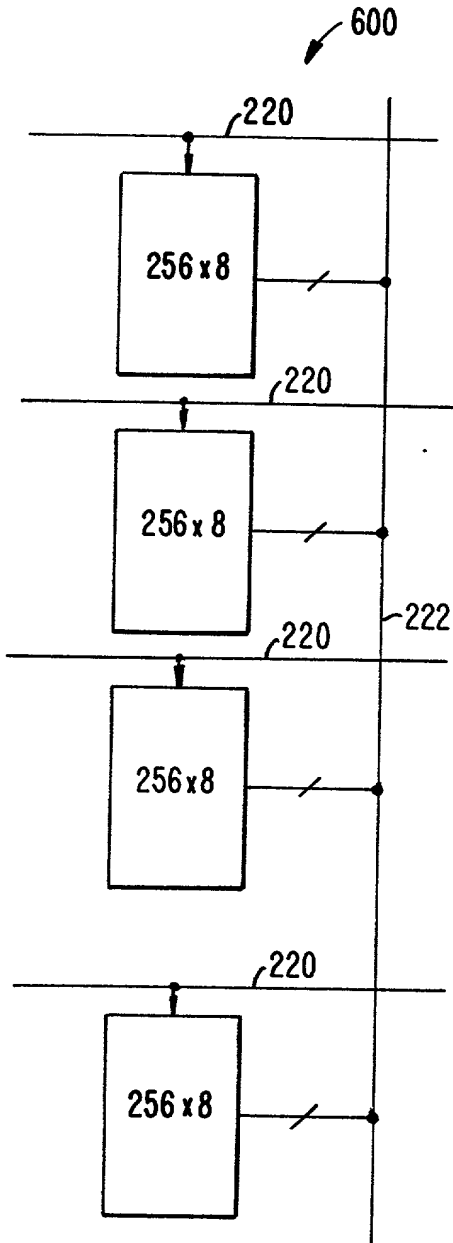


FIG. 23A.

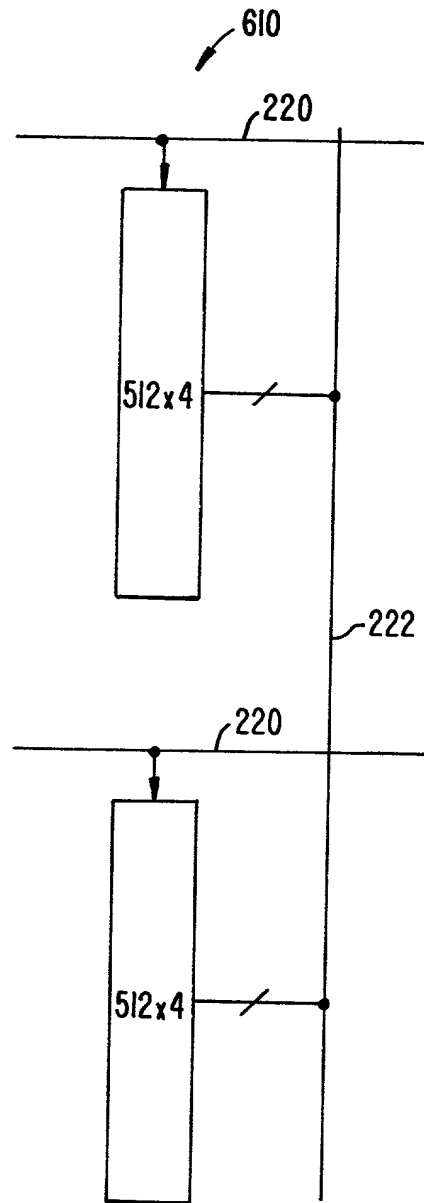


FIG. 23B.



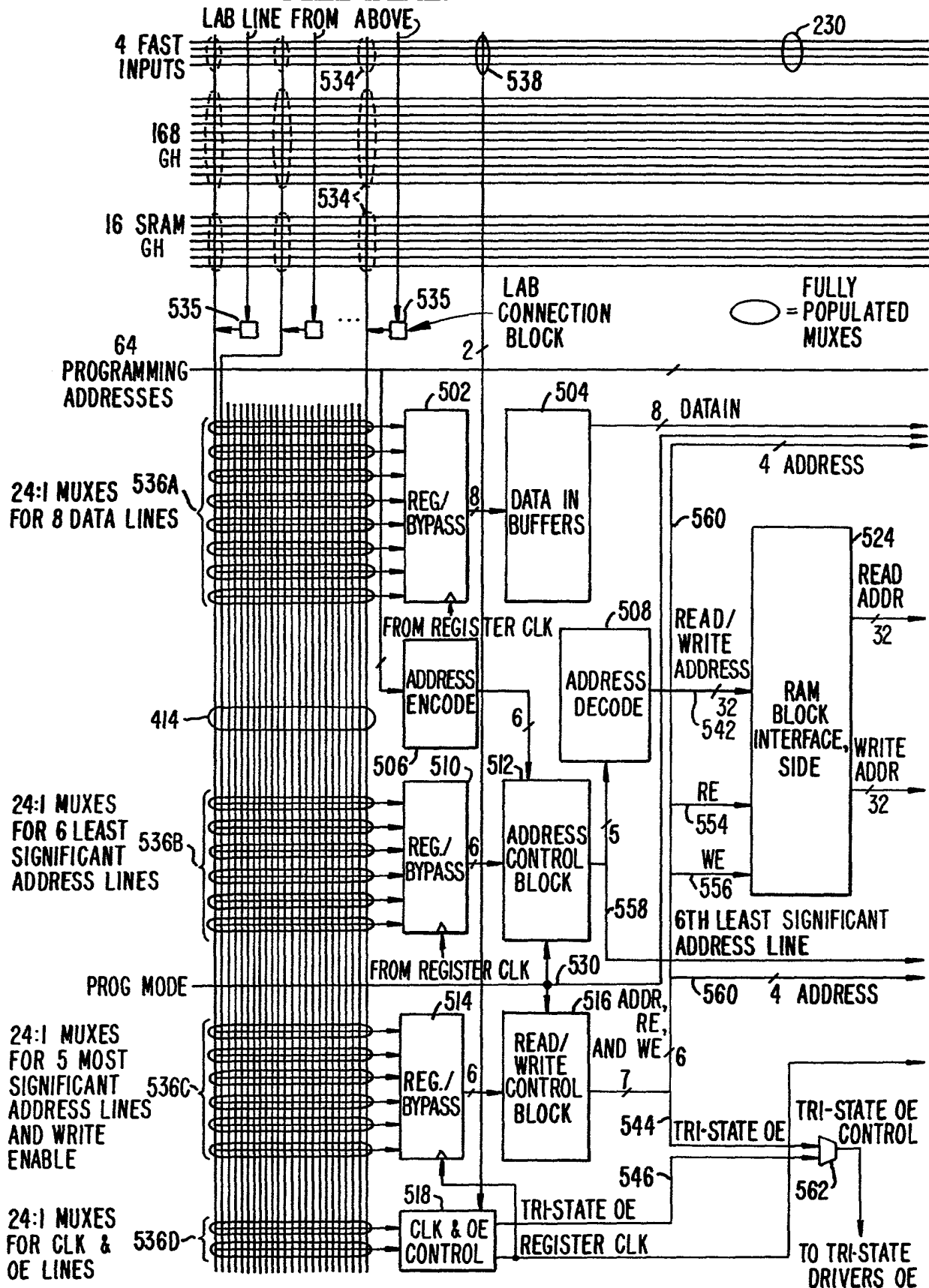


FIG. 24A.

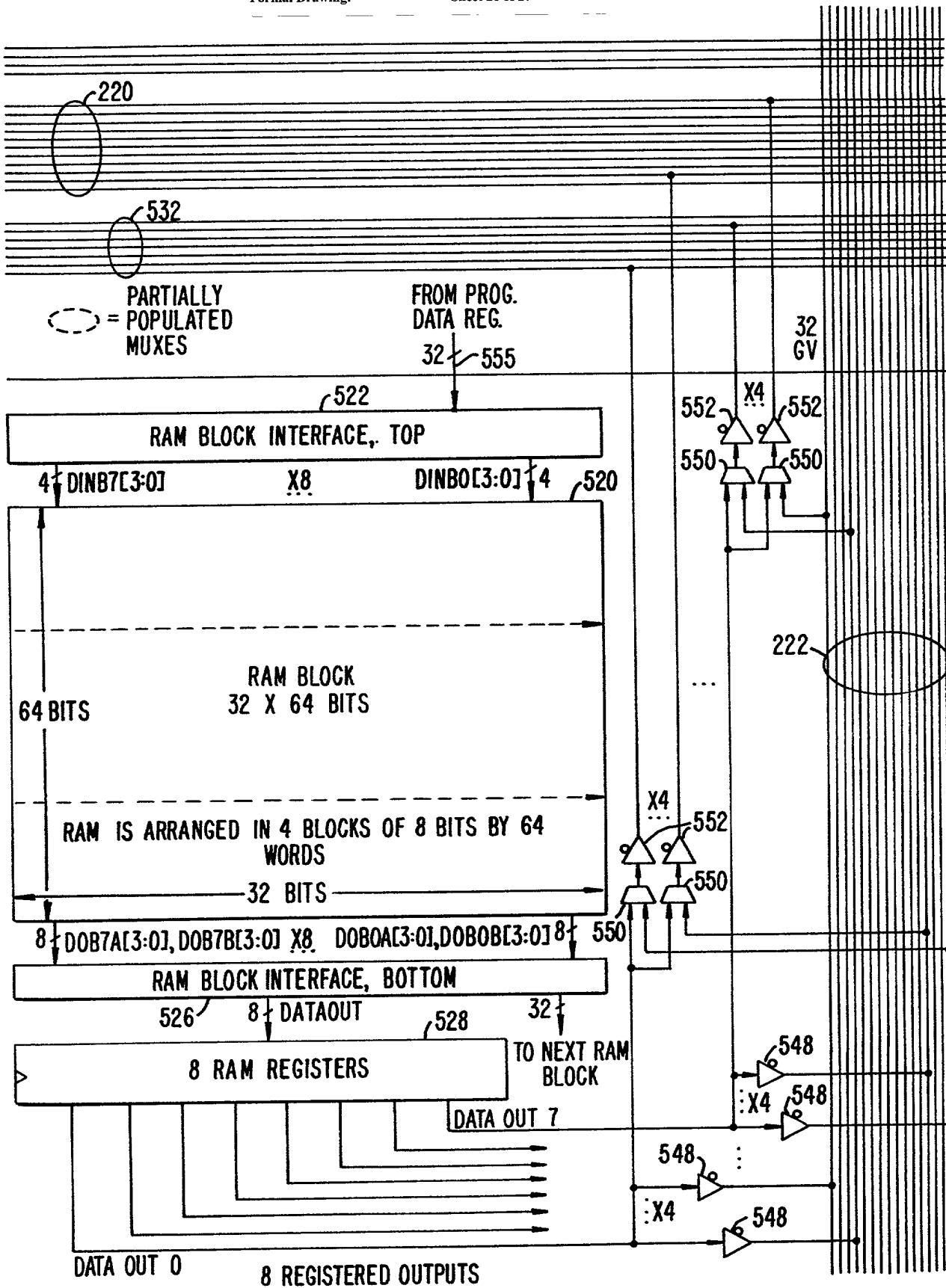
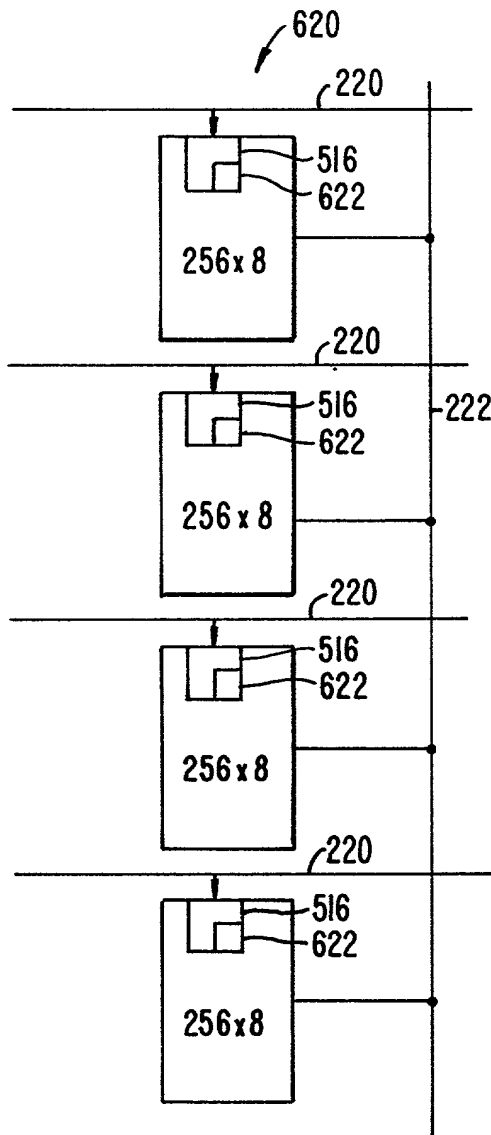
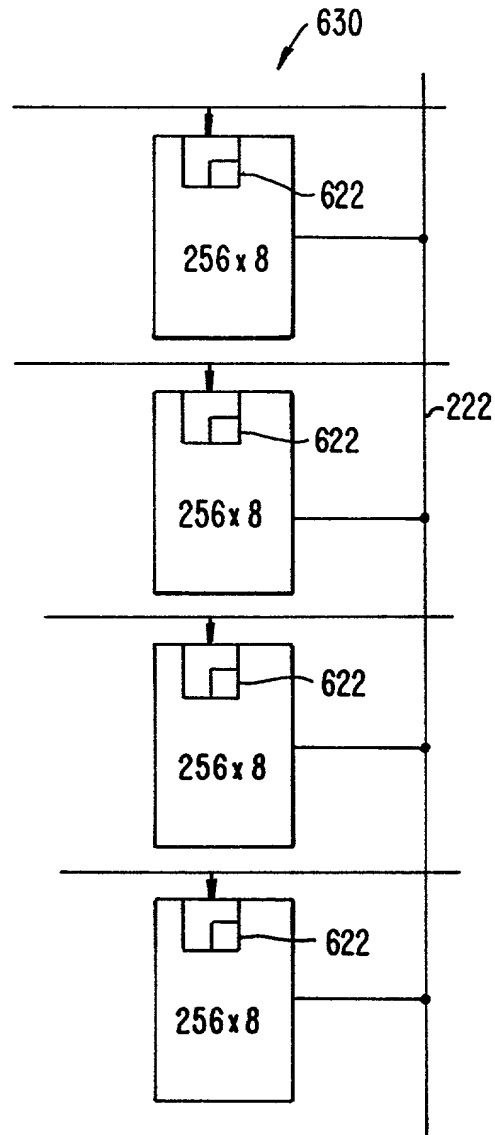


FIG. 24B.



**FIG. 25A**



**FIG. 25B.**